

/715869

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|----------------------|----------|
| 209                  | 573      |
| Class                | Subclass |
| ISSUE CLASSIFICATION |          |

PRIORITY  
NUMBER  
715869

PATENT DATE

PATENT  
NUMBERSERIAL NUMBER  
08/715,869  
REISSUEFILING DATE  
09/19/96CLASS  
209

SUBCLASS

573

GROUP ART UNIT

310  
3653

EXAMINER

Nguyen

APPLICANTS

STEVE W. HEPPLER, BOISE, ID.

\*\*CONTINUING DATA\*\*\*\*\*

VERIFIED THIS APPLN IS A RE OF 08/046,246 04/13/93 PAT 5,348,164

Yes TNN

6/17/98

\*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\*

VERIFIED

None TNN

6/17/98

FOREIGN FILING LICENSE GRANTED 01/09/97

Foreign priority claimed  
35 USC 119 conditions met☐ yes  
☒ noAS  
FILEDSTATE OR  
COUNTRYSHEETS  
DRWGS.TOTAL  
CLAIMSINDEP.  
CLAIMSFILING FEE  
RECEIVEDATTORNEY'S  
DOCKET NO.

Verified and Acknowledged

TNN  
Examiner's Initials

ID

4

32

13

\$1,819.00

2972US (92-04)

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TITLE

METHOD AND APPARATUS FOR TESTING INTEGRATED CIRCUITS

U.S. DEPT. OF COMM./PAT. &amp; TM-PTO-438L (Rev.12-94)

PARTS OF APPLICATION  
FILED SEPARATELY

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due

Date Paid

Label  
Area

Assistant Examiner

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PRIMARY EXAMINER

Primary Examiner

PREPARED FOR ISSUE

Applications Examiner

CLAIMS ALLOWED

Total Claims

Print Claim

32

7

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

4

4

2

ISSUE  
BATCH  
NUMBER

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Form PTO-438A  
(Rev. 8/92)

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